## No title available

Publication number: JP5266272

**Publication date:** 

1993-10-15

Inventor:
Applicant:
Classification:

- international: G06F1/24; G06K19/07; G06F1/24;

G06K19/07; (IPC1-7): G06K19/07;

G06F1/24

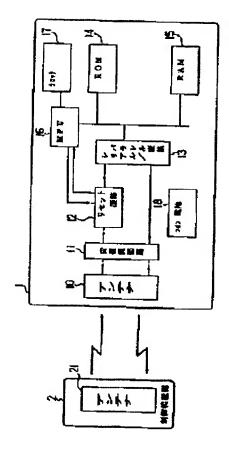
- European:

**Application number:** JP19920063777 19920319 **Priority number(s):** JP19920063777 19920319

## Report a data error here

## Abstract of JP5266272

PURPOSE:To unnecessitate reset work after manufacturing and to stop runaway by setting the control part of an IC card at a reset state by a reset circuit, and releasing the reset state by an operation signal from an MPU. CONSTITUTION: The reset circuit 12, when receiving a startup signal from an antenna 21 on a card processor 2 side, is operated so as to be turned off for constant time. When a reset OFF state is set, the operation of a control part program in the IC card is started, and the operation signal is outputted at constant period as performing communication with the card processor 2, and the reset circuit 12 prevents the control part of the IC card 1 from being set in the reset state. When the communication with the card processor 2 side is completed



and an operation is completed in a normal operation, no operation signal is outputted, and the reset circuit 12 sets the IC card in the reset state, and the operation of the program can be started again by the start-up signal on the card processor 2 side. When a runway state occurs, no operation signal is outputted, which stops the runaway.

Data supplied from the **esp@cenet** database - Worldwide